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PATENT NO:
US 6,963,312 B2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patentee: Nicholas A. Schuneman et al.
U.S. Patent No.: US 6,963,312 B2
Issue Date: November 8, 2005
Serial No.: 10/022,753
Filing Date: December 14, 2001
Confirmation No.: 2544
Title: SLOT FOR DECADE BAND TAPERED SLOT ANTENNA, AND
METHOD OF MAKING AND CONFIGURING SAME

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

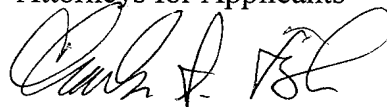
Dear Sir:

REQUEST FOR CERTIFICATE OF CORRECTION
UNDER 37 CFR § 1.322

It is respectfully requested that a Certificate of Correction be issued in accordance with the enclosed Form PTO-1050. The error involved is believed to be a Patent Office error, and it is believed that no fee is due in association with this request for a Certificate of Correction. However, the Commissioner is hereby authorized to charge any fees or credit any overpayments to Deposit Account No. 02-0384 of Baker Botts L.L.P.

It is respectfully submitted that a significant error is present in the printed patent, that correction thereof in accordance with the enclosed Form PTO-1050 is required in order that no misunderstanding will occur.

Respectfully submitted,
BAKER BOTTS L.L.P.
Attorneys for Applicants



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**UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION**

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Dated: November 8, 2005
Inventor(s): Nicholas A. Schuneman et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 5, Line 28:

After "14" delete "7.30E-30" and insert -- -7.30E-30 --.

Column 5, Line 32:

After "20" delete "1.30E-50" and insert -- -1.30E-50 --.

Column 13, Line 55:

After "length" delete "l" and insert -- ℓ --.

Column 14, Line 18:

After "follows" please delete $\begin{bmatrix} A & B \\ C & D \end{bmatrix}_{SEG} = \begin{bmatrix} \cos(\beta l) & jZ_{SEG} \sin(\beta l) \\ \frac{j \cdot \sin(\beta l)}{Z_{SEG}} & \cos(\beta l) \end{bmatrix}$

and insert -- $\begin{bmatrix} A & B \\ C & D \end{bmatrix}_{SEG} = \begin{bmatrix} \cos(\beta \ell) & jZ_{SEG} \sin(\beta \ell) \\ \frac{j \cdot \sin(\beta \ell)}{Z_{SEG}} & \cos(\beta \ell) \end{bmatrix}$ --.

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Column 14, Line 57:

After "x[" delete " Y_{FS} " and insert -- V_{FS} --.

Column 15, Line 9:

After "current times impedance. Thus" delete " $V_{FS}=I_{FS}Z_{FS}$ " and insert -- $V_{FS} = I_{FS} \cdot Z_{FS}$ --.

Column 20, Line 44:

After "an elongate conductive element" delete "which extends" and insert -- being coplanar with the conductive section, the elongate conductive element extending --.

Column 21, Line 1:

Delete Claim 5 and insert

-- An apparatus, comprising:

a conductive section having a recess which includes a balun portion and a slot portion, said slot portion communicating at one end with said balun portion, and said slot portion having edges on opposite sides thereof which each follow a predetermined curve other than a first-order exponential curve; and

an elongate conductive element which extends generally transversely with respect to said slot portion in the region of said one end thereof, and which can carry an electrical signal;

wherein said predetermined curve includes first and second exponential characteristics involving respective different exponential powers. --

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Column 21, Line 5

Delete Claim 6 and insert --

An apparatus, comprising:

a conductive section having a recess which includes a balun portion and a slot portion, said slot portion communicating at one end with said balun portion, and said slot portion having edges on opposite sides thereof which each follow a predetermined curve other than a first-order exponential curve; and

an elongate conductive element which extends generally transversely with respect to said slot portion in the region of said one end thereof, and which can carry an electrical signal;

wherein said predetermined curve includes a plurality of exponential characteristics involving respective different exponential powers. --.

Column 21, Line 9:

Delete Claim 7 and insert --

An apparatus, comprising:

a conductive section having a recess which includes a balun portion and a slot portion, said slot portion communicating at one end with said balun portion, and said slot portion having edges on opposite sides thereof which each follow a predetermined curve other than a first-order exponential curve; and

an elongate conductive element which extends generally transversely with respect to said slot portion in the region of said one end thereof, and which can carry an electrical signal;

a dielectric layer wherein said conductive section includes two electrically conductive layers disposed on opposite sides of said dielectric layer, said conductive layers having respective recesses therein which are aligned with each other and which each include a balun hole that is part of said balun portion and a slot that is part of said slot portion; and

wherein said conductive section includes a plurality of vias which each extend between said conductive layers through said dielectric layer, said vias being disposed near each edge of each said slot at spaced locations therealong. --

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